

◆ Product Description

Micable SA-07-4B017022 is a high performance 4x4 butler matrix, covering the frequency of 1.7-2.2GHz. It can transfer the signal reciprocally from any of 4 ports to any of 4 ports, with super phase accuracy, amplitude balance, very small insertion loss and high port to port isolation. The system comes housed in a compact, 101.6x106.7x16.5mm cavity. Because the high performance passive components and cables are used inside, the system has very stable, repeatable performance.

◆ Key Features

Feature	Advantage
Super phase accuracy	The system has typical 2° phase accuracy over optimized frequency range, it can be used as accurate phase feed network to realize ideal beamforming performance of phase array.
Excellent amplitude balance	The system has typical 0.3dB amplitude balance over optimized frequency range, it can help realize the ideal vector combination of the signal and beamforming.
Low insertion loss	The system has very low 7dB max insertion loss (including theoretical 6dB loss), it can help test system increase the dynamic range.
High port to port isolation	This can reduce the interference between the adjacent channel signals.
Low VSWR	Can better match the 50Ω system, reduce the reflection of the signal and energy loss.
High Power	Every port can accept the input signal with CW power of 5W, it is good for big signal measurement.
Excellent performance stability and repeatability	Maintain the consistent system performance, reduce the need of calibration.

◆ Specifications

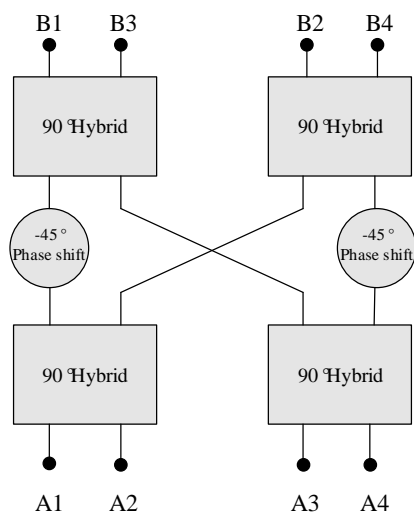
Specifications	Unit	Limits	Typ.
Frequency Range	GHz	1.7-2.2	
VSWR for all RF ports	:1/(Max.)	1.3	1.2
Insertion Loss	dB/(Max.)	7	6.4
Amplitude Balance	dB/(Max.)	±0.5	±0.3
Amplitude Flatness per path	dB/(Max.)	±0.4	±0.3
Phase Accuracy	Deg./(Max.)	±3	±2
Isolation	dB/(Min.)	18	22

- **Average Power:** 5W Max (single Input-Port);
- **Connector:** SMA[F];
- **Case Style:** 101.6x106.7x16.5mm;
- **Weight(max.):** 410g;

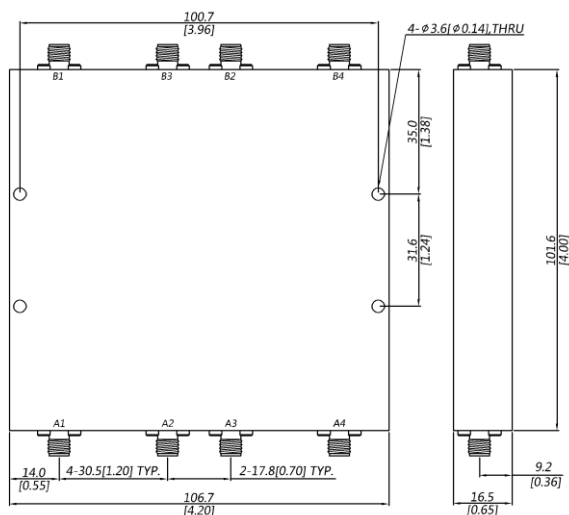
◆ Phase Table

Input Output	A1	A2	A3	A4
B1	-45	-135	-90	-180
B2	-90	0	-225	-135
B3	-135	-225	0	-90
B4	-180	-90	-135	-45

◆ Schematic Diagram

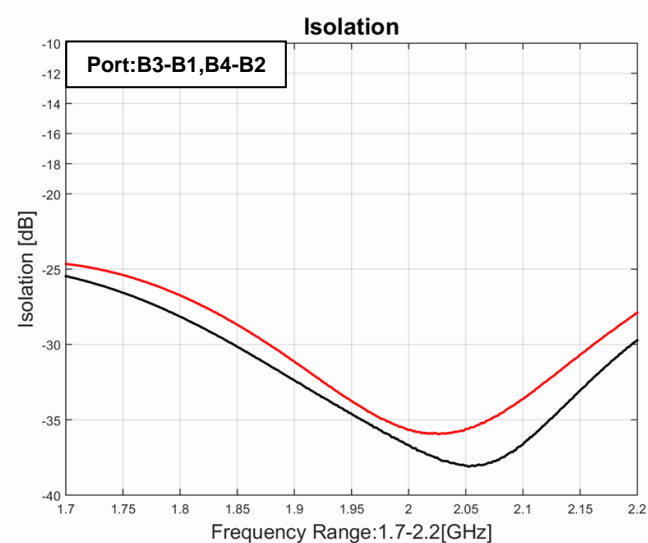
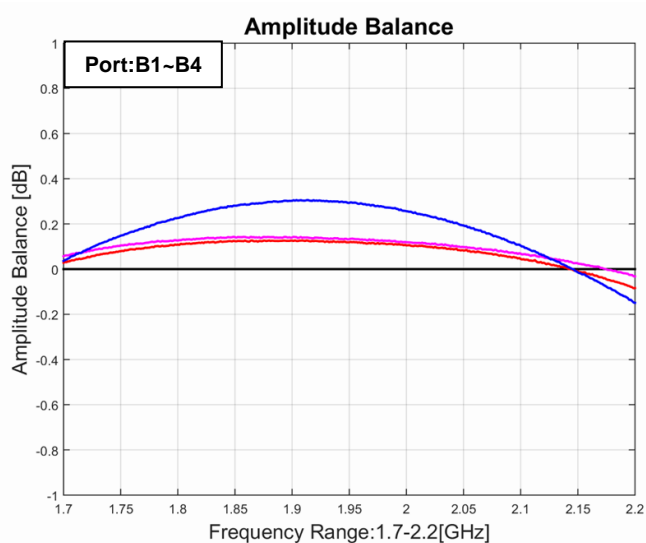
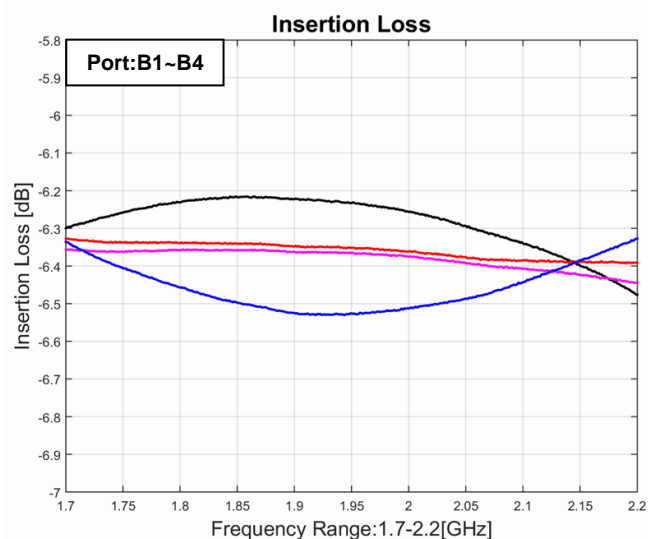
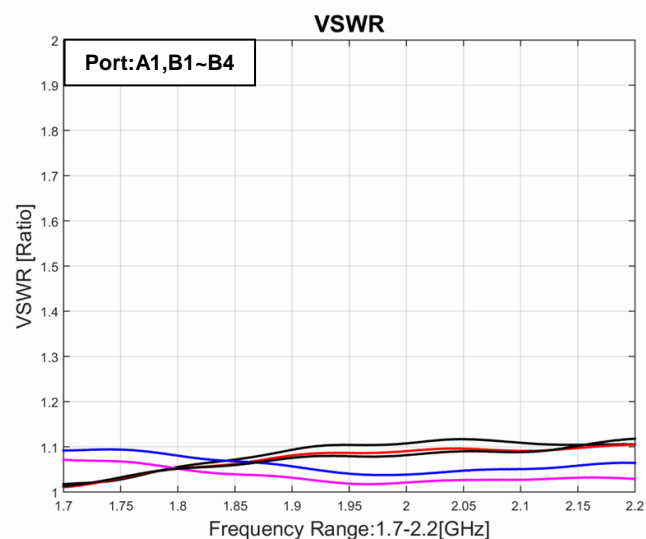


◆ Outline Drawing

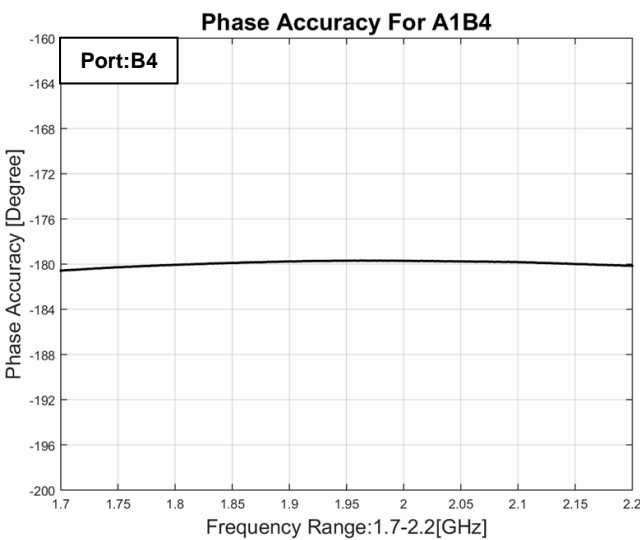
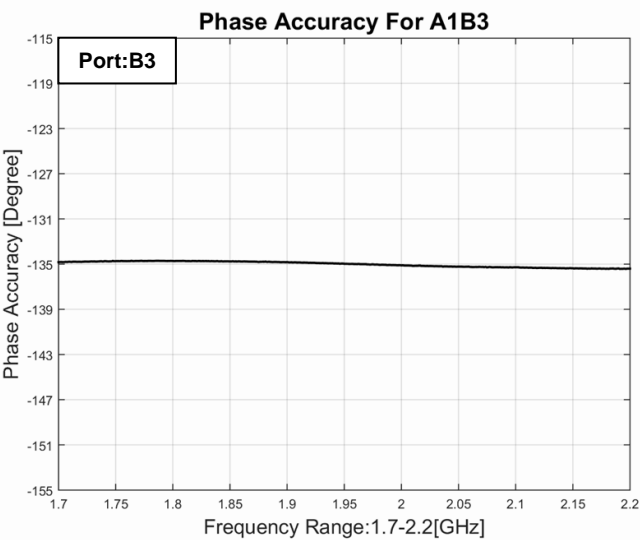
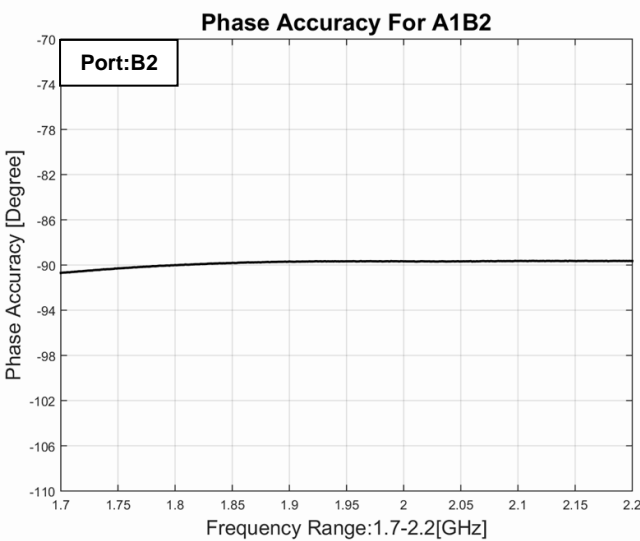
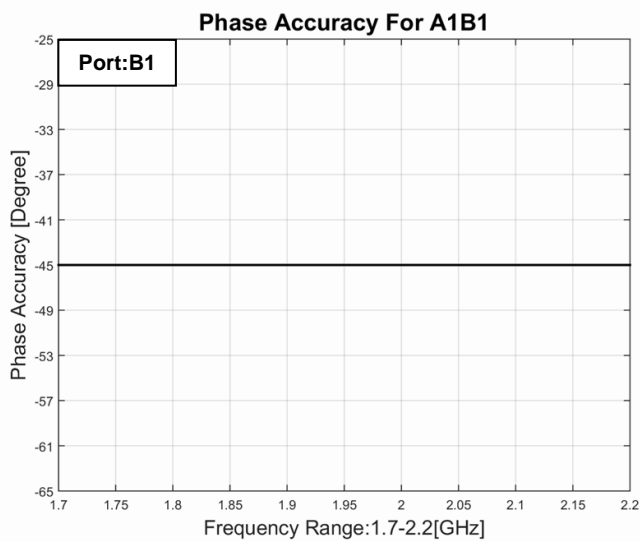


◆ Typical Tested Graph

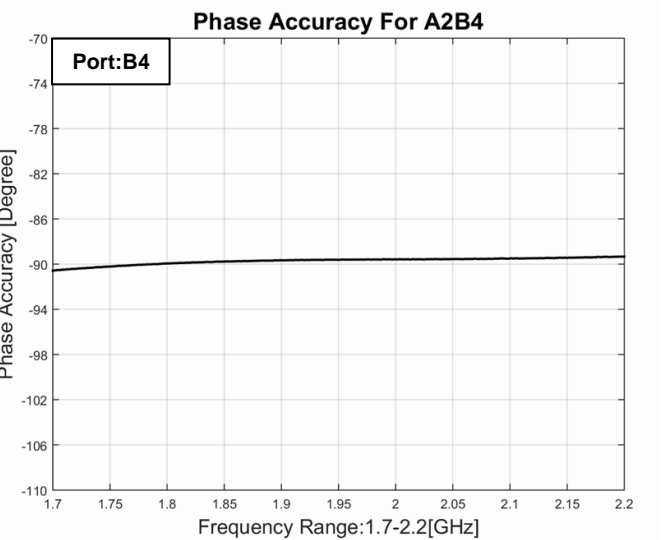
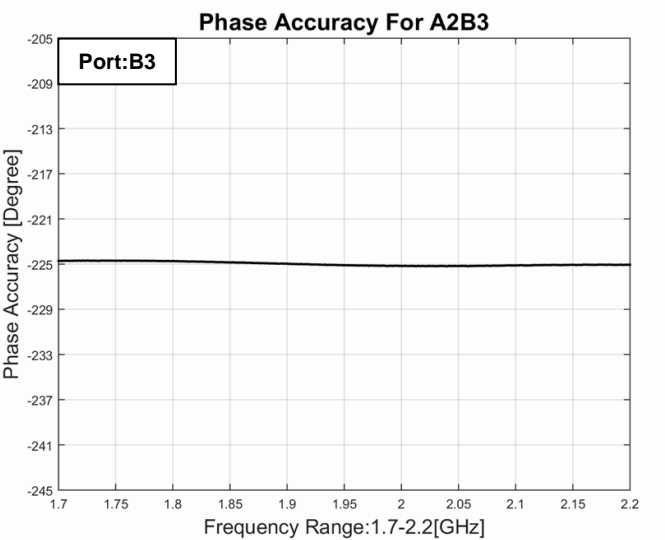
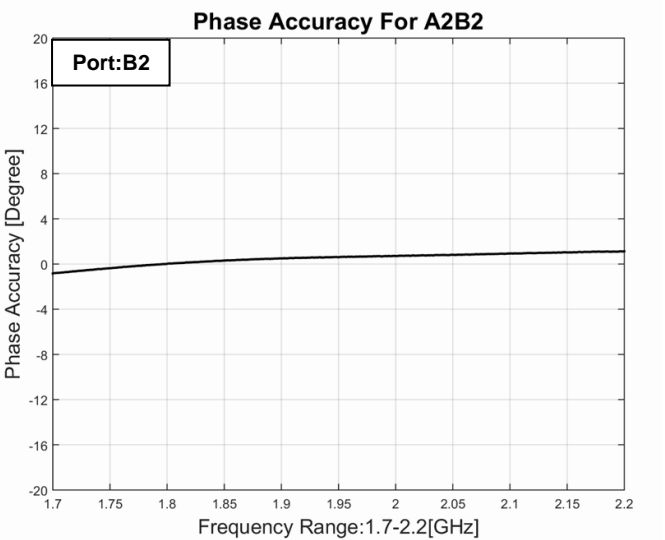
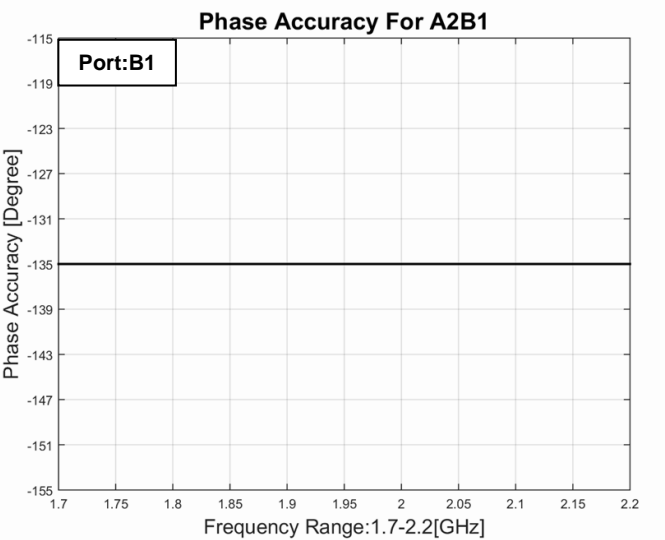
Take A1 As The Input Port



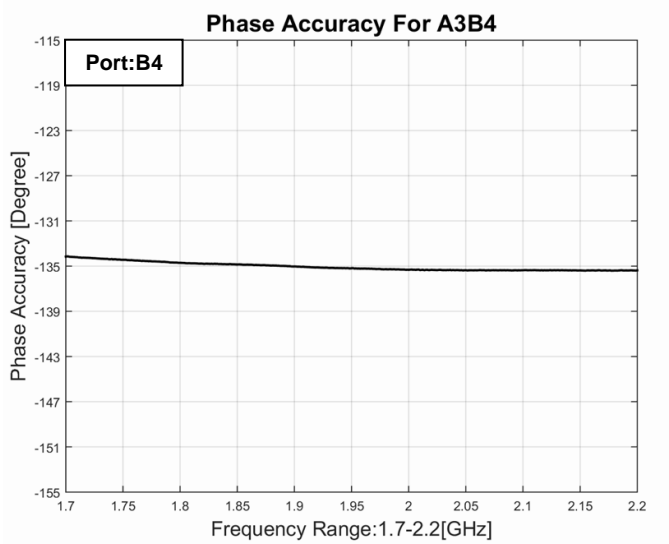
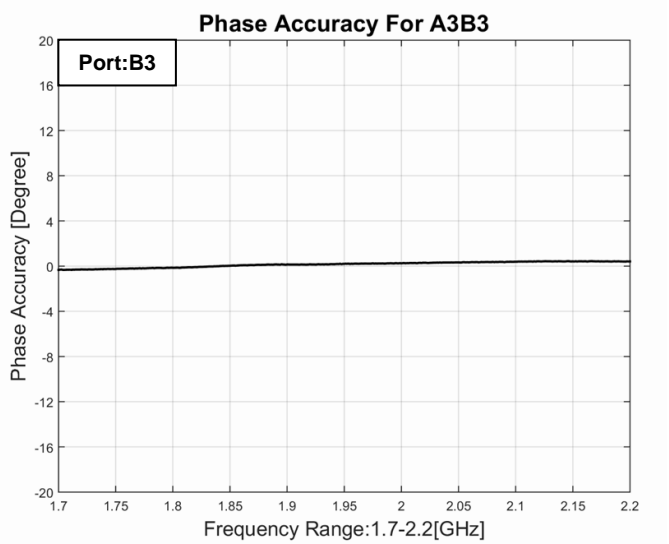
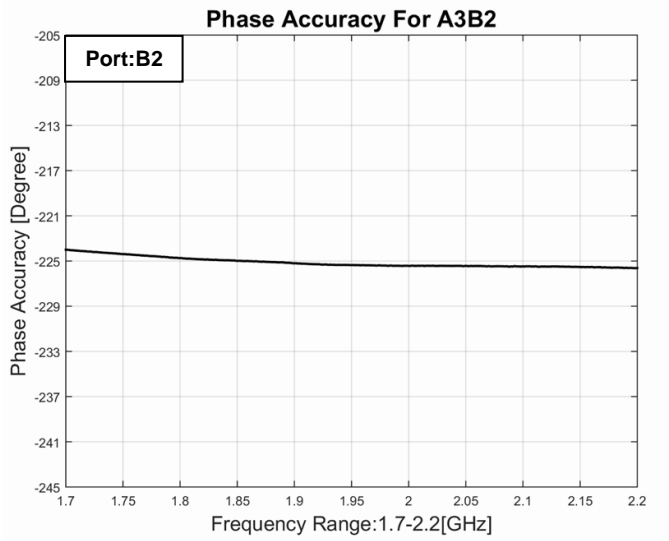
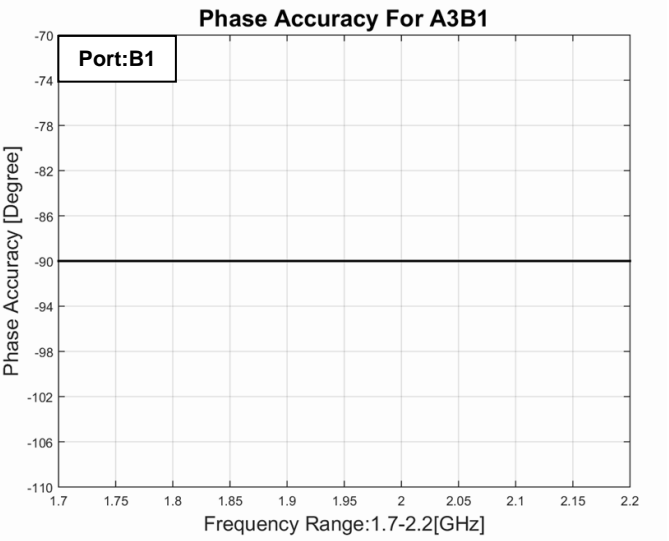
Take A1 As The Input Port



Take A2 As The Input Port



Take A3 As The Input Port



Take A4 As The Input Port

